

JUN 16 2006

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.  
FIS920030412US1

In Re Application Of: Marwan H. Khater, et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/709,220	04/22/2004	Michelle Estrada	32,074	2826	3219


Invention: **STRUCTURE AND METHOD OF FORMING BIPOLAR TRANSISTOR HAVING A SELF-ALIGNED RAISED EXTRINSIC BASE USING SELF-ALIGNED ETCH STOP LAYER**

Address to:  
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P.O. Box 1450  
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Transmitted herewith are:

12 sheets of formal drawing(s) for this application.

☐ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

  
Signature

Dated: May 11, 2006

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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